

94

B/IFW

Apr 19 2005 10:01AM PHILIPS IP&S SAN JOSE, CA 4084749080

p. 1



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First-Named Inventor: Petrus H. C. Magnee
Application No.: 09/972,576 Conf.: 4658
Date Filed: 10/05/2001
Notice of Allowance Date:

Docket No.: NL00 00549
Art Unit: 2811
Examiner: Junghwa Im
Customer No.: 24738

Title: PROTECTION DIODE FOR IMPROVED RUGGEDNESS OF A RADIO
FREQUENCY POWER TRANSISTOR AND SELF-DEFINING METHOD TO
MANUFACTURE SUCH PROTECTION DIODE

Box ISSUE FEE
Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

LETTER TO OFFICIAL DRAFTSMAN

Sir:

Enclosed please find two (2) sheets of corrected/substitute drawings for entry in the
above-referenced patent application.

Please charge any fees which may be required, except the issue fee, or credit any
overpayment to Deposit Account No. 14-1270.

Date: 18-APR-2005

Respectfully submitted,

By

Peter Zawilski, Reg. No. 43,305
(408) 474-9063
Philips Electronics North America Corp.
1109 McKay Drive MS-41SJ
San Jose, California 95131

CERTIFICATE OF MAILING OR TRANSMISSION

I hereby certify that on the date indicated below this correspondence is being
faxed to or deposited with the United States Postal Service with sufficient postage
for first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box
1450, Alexandria, VA 22313-1450".

(Date)

19-Apr-05

(Signature)

(Name)

Daniel L. Michalek